



Logically Determined Design: Clockless System Design with NULL Convention Logic

By Karl Fant

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This seminal book presents a new logically determined design methodology for designing clockless circuit systems. The book presents the foundations, architectures and methodologies to implement such systems. Based on logical relationships, it concentrates on digital circuit system complexity and productivity to allow for more reliable, faster and cheaper products.

- * Transcends shortcomings of Boolean logic.
- * Presents theoretical foundations, architecture and analysis of clockless (asynchronous) circuit design.
- * Contains examples and exercises making it ideal for those studying the area.

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Editorial Review

From the Back Cover

A Revolutionary New Digital System Design Methodology

Logically determined electronic system design is a viable solution to manage the growing complexity of digital design in today's industry. The author shows how this new clockless design methodology is essential for securing the future economic growth of the digital design industry by removing a costly design factor and opening the door to more reliable and less expensive products. Key features include:

- An explanation of why Boolean logic is an inadequate conceptual foundation for digital design; how it is compromising the growth of the digital design industry; and why logically determined design is the necessary solution
- A conceptual derivation of NULL Convention Logic™ and the basic principles of logically determined design
- A complete introduction to the basic structures of logically determined design and the methods of composing these structures into a logically determined system
- Performance analysis techniques of a logically determined system and the methodology of optimal system design

Following the author's logical progression of topics and attention to detail, learn how to design a digital system solely in terms of logical relationships, transcending issues of critical timing and simplifying the design and manufacture of robust and reliable large-scale digital systems.

Not afraid to be controversial, this book successfully challenges the current theoretical foundations of digital system design. This is essential reading for all researchers, practitioners, engineers, and students in the fields of computer system design and electrical engineering.

About the Author

KARL M. FANT is President of Theseus Research, Inc. After serving as a Research Fellow at Honeywell, he co-founded Theseus Research and then Theseus Logic, which is now commercializing NULL Convention Logic™. He holds twenty-nine patents and has published numerous papers.

Users Review

From reader reviews:

Carl Yeates:

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